

A chip carrier is provided with a carrier base having an opening and being capable of accommodating a chip inside the opening, and an outer lid for closing the opening of the carrier base. The outer lid is engaged with the carrier base when rotated in a space of the carrier base formed over the opening. Hence it is possible to provide a chip carrier that is composed of only a small number of parts, has a relatively simple mechanism, is inexpensive, and facilitates automation of assembling and sharing of test equipment for semiconductor devices, and provides high test efficiency.